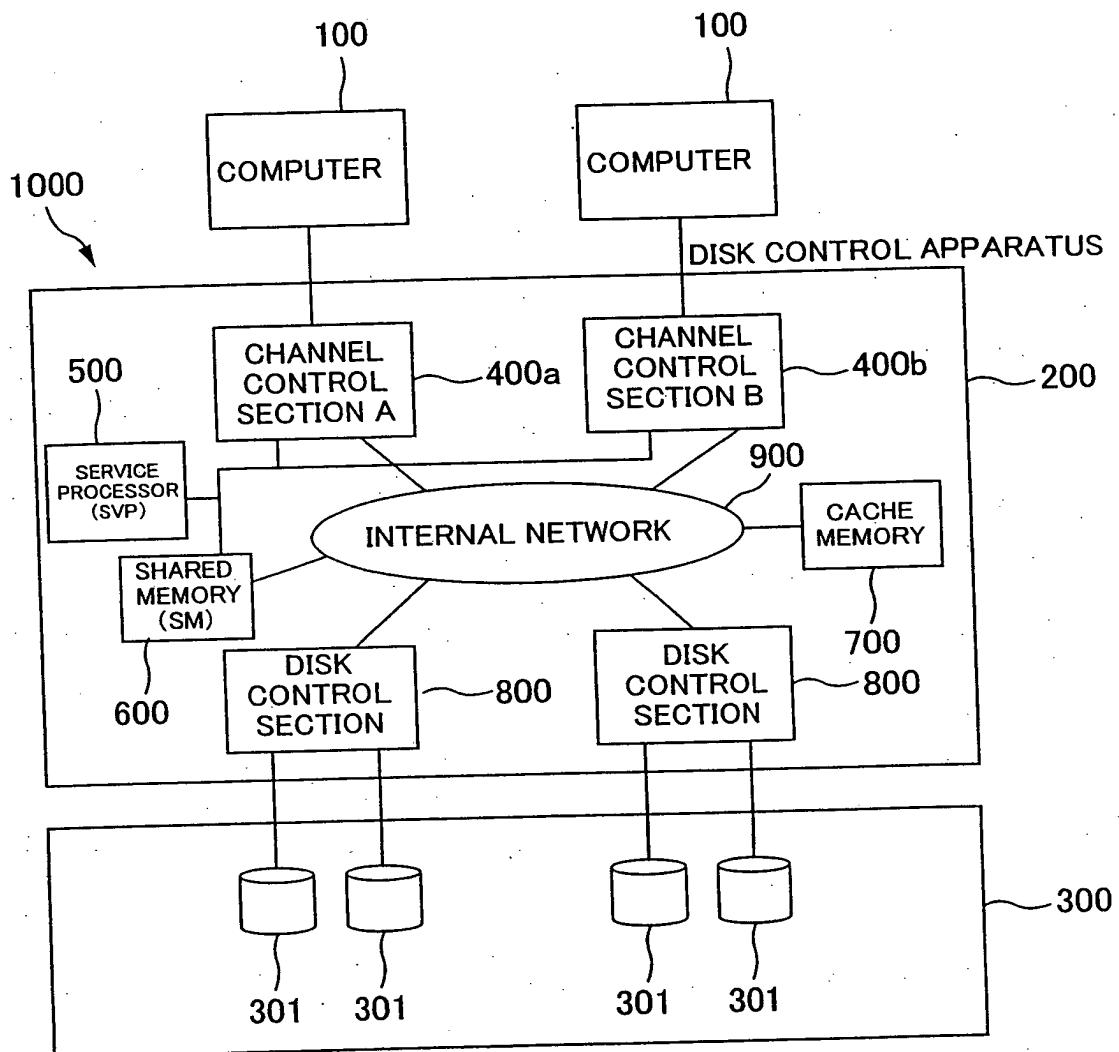


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FIG. 1



Applicant: Katsuhiro UCHIUMI, et al.

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FIG.2A

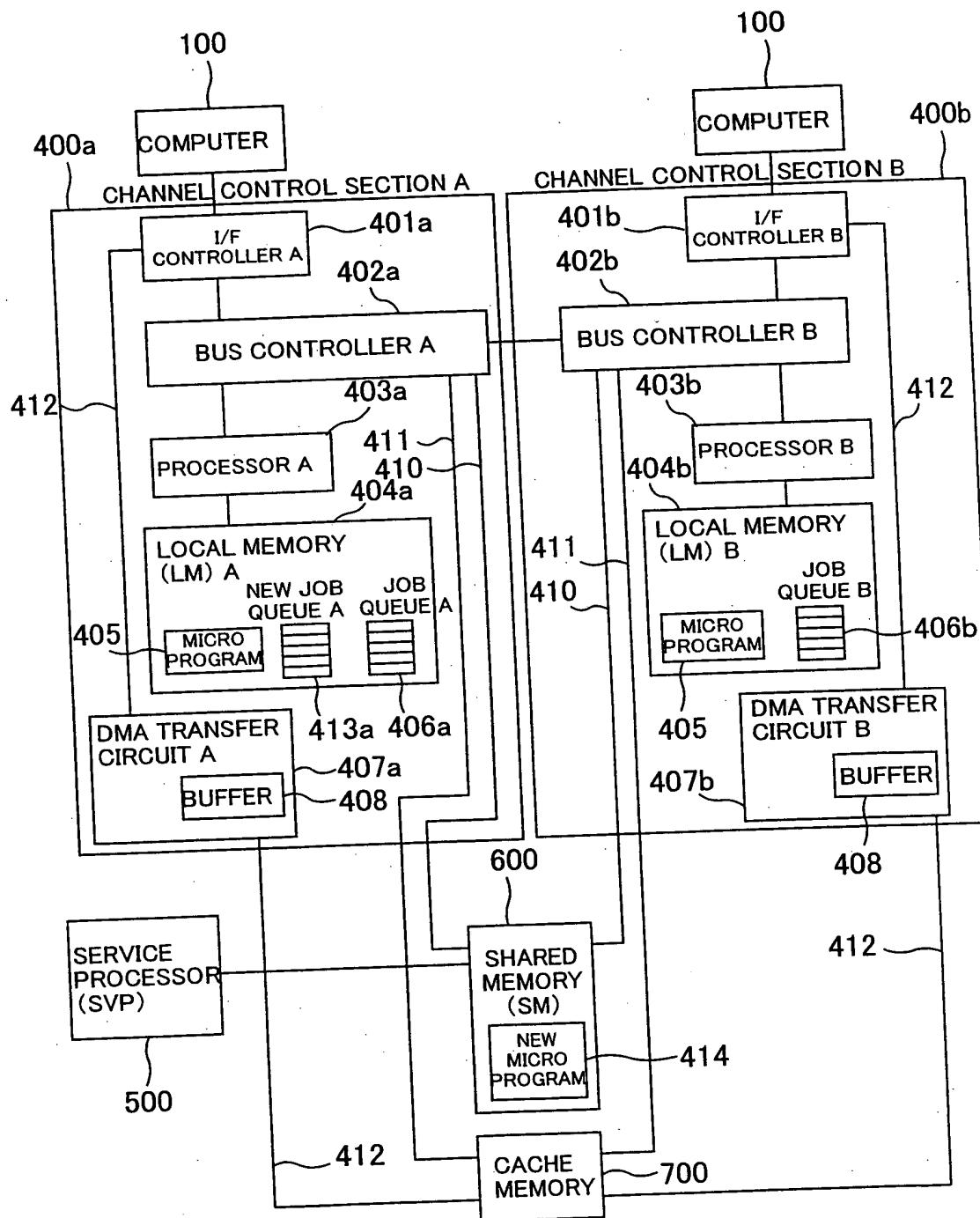


FIG.2B

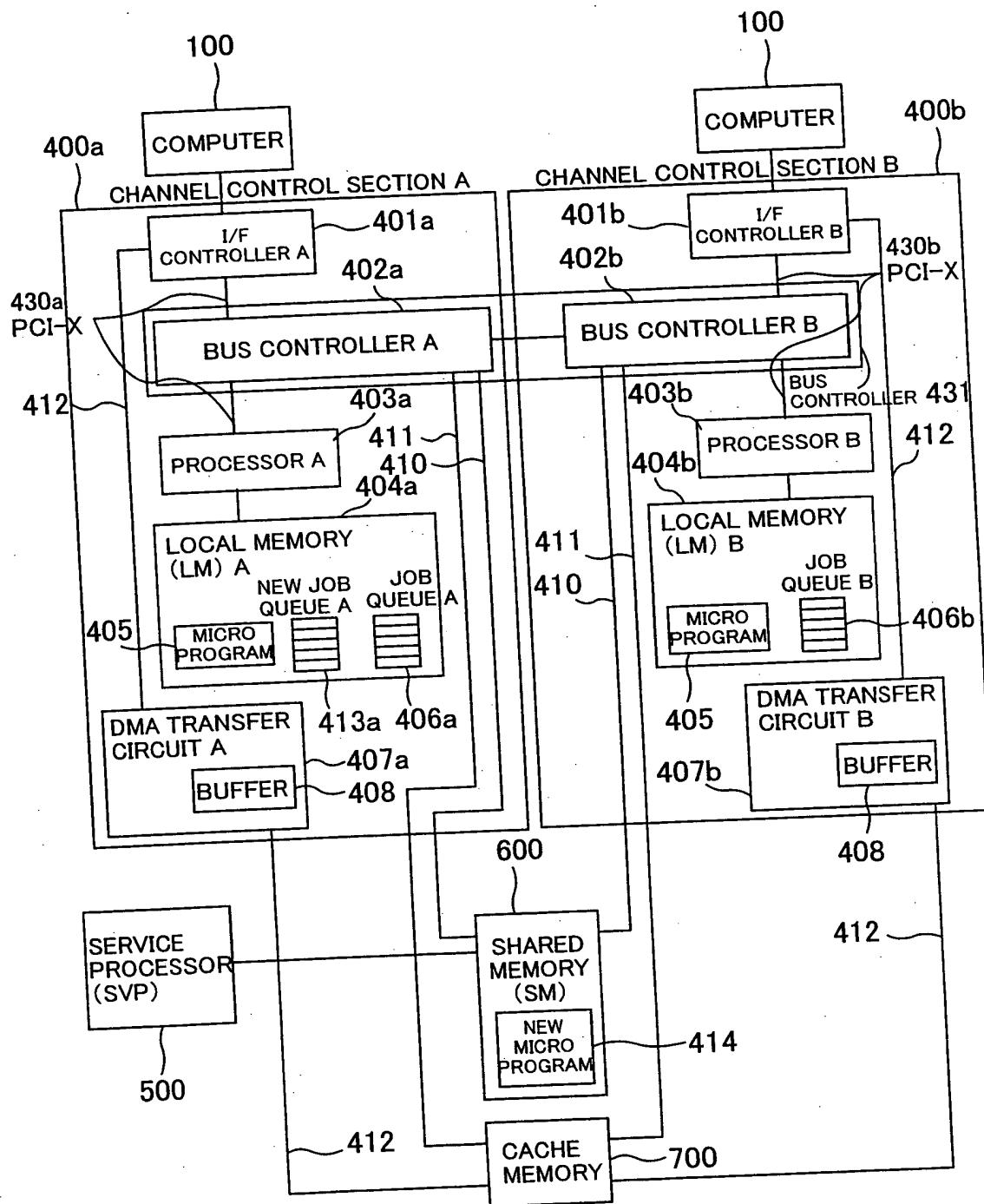


FIG.3

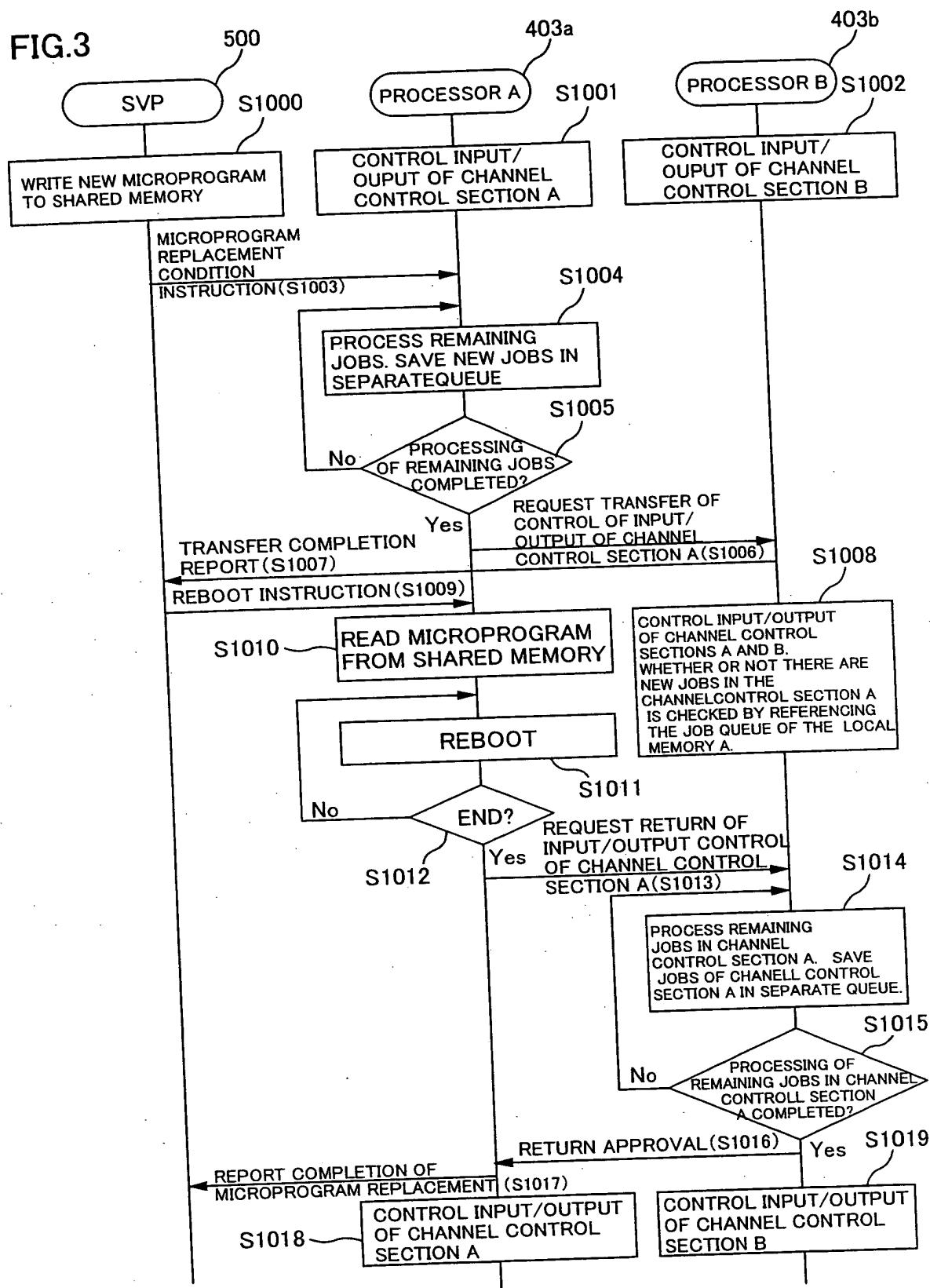


FIG.4

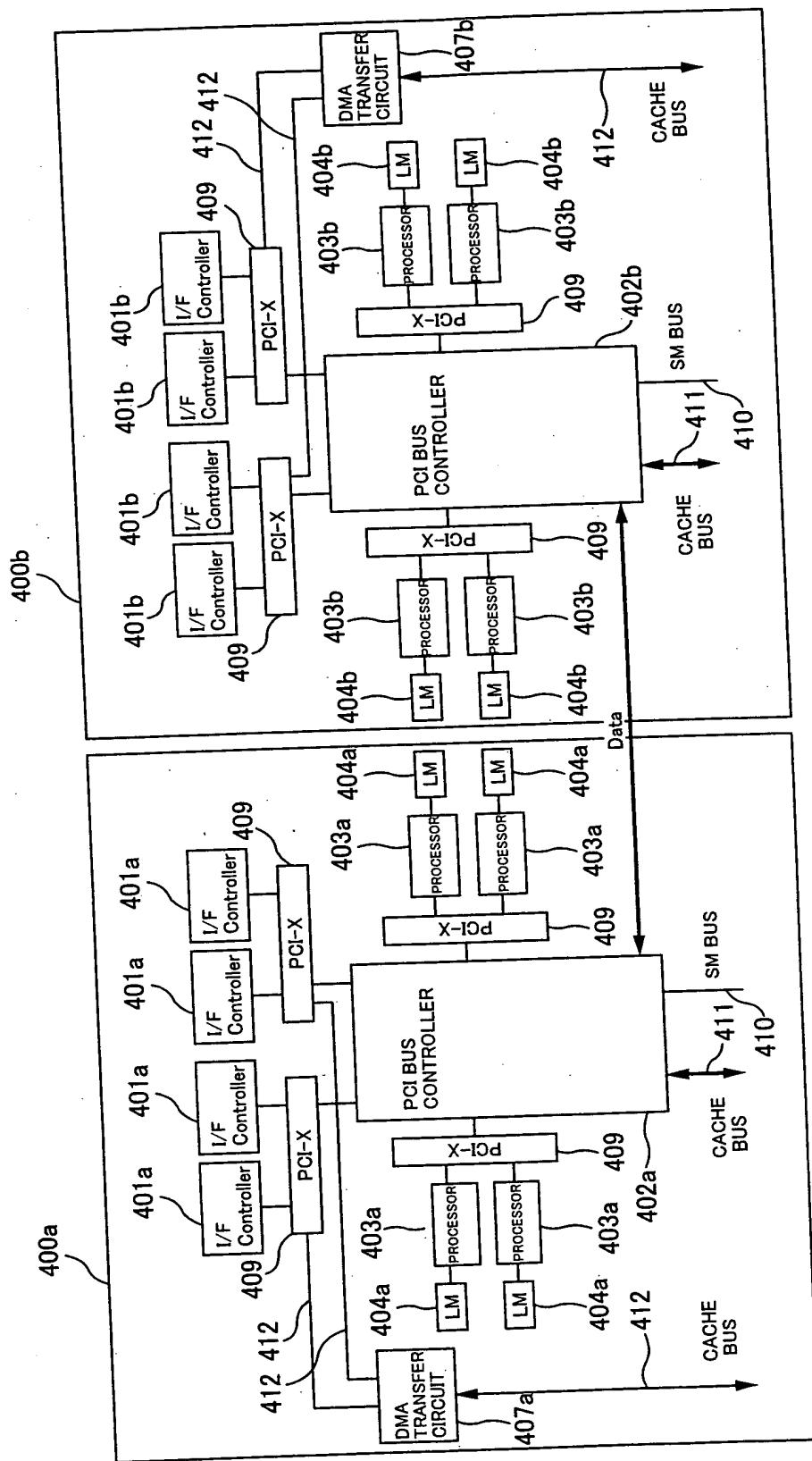
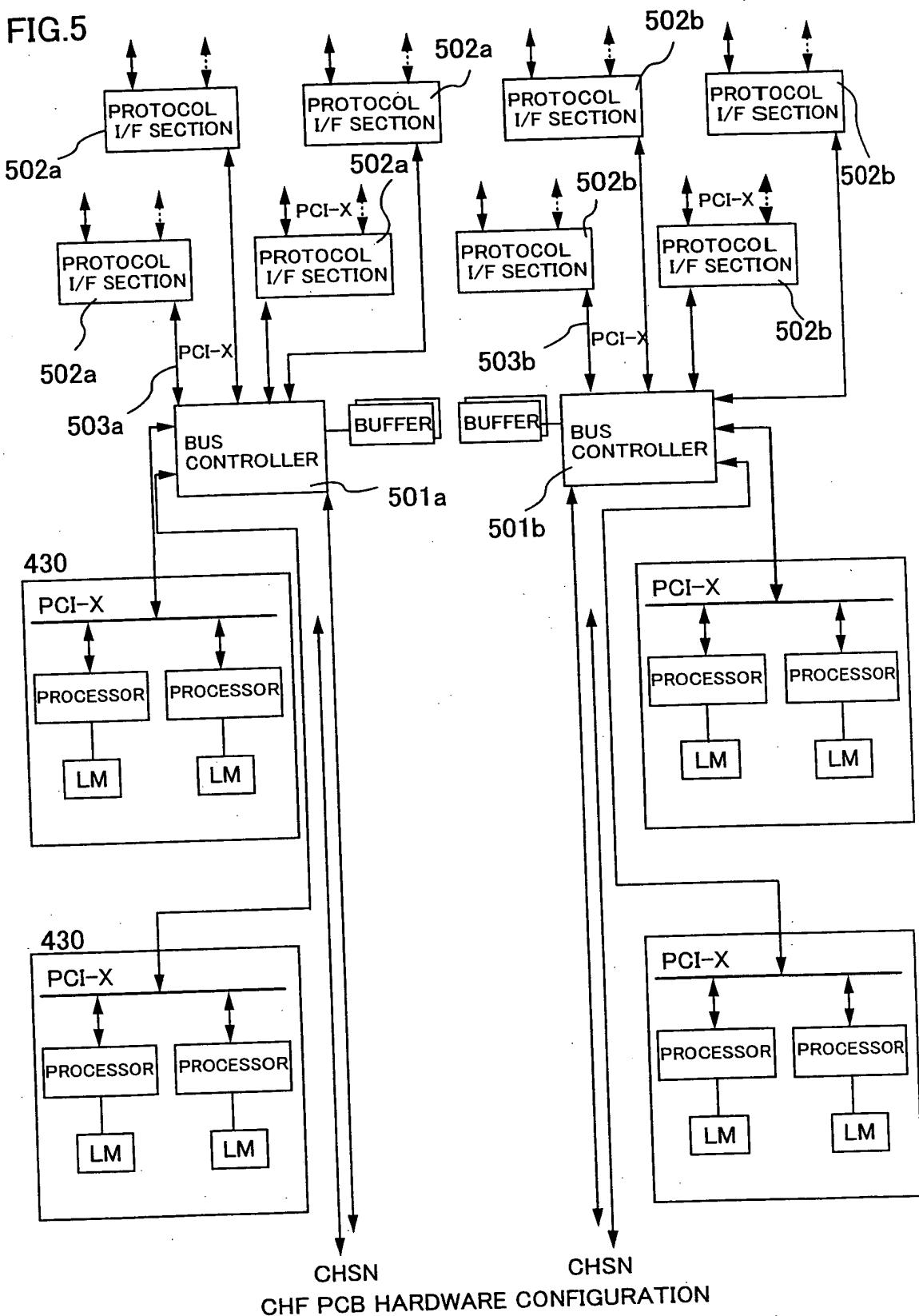
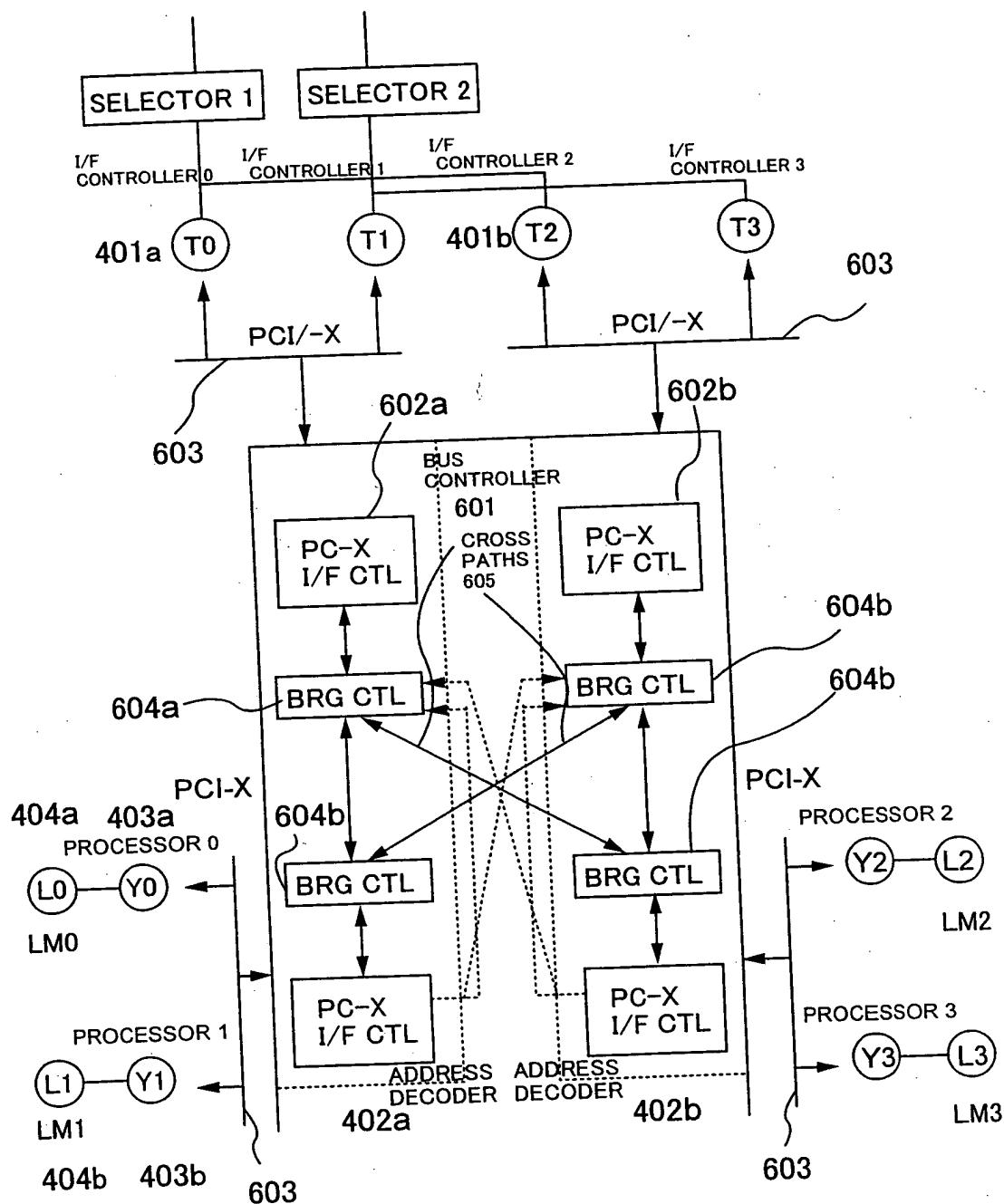


FIG.5



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FIG.6



PCI-X SECTION HARDWARE CONFIGURATION